LIQUID CRYSTAL DISPLAY MODULE

(L C M) COUNTER DRAWING

MESSRS: **U R T – S T D**

MODEL NO: **UMNH-7659MD-3T**

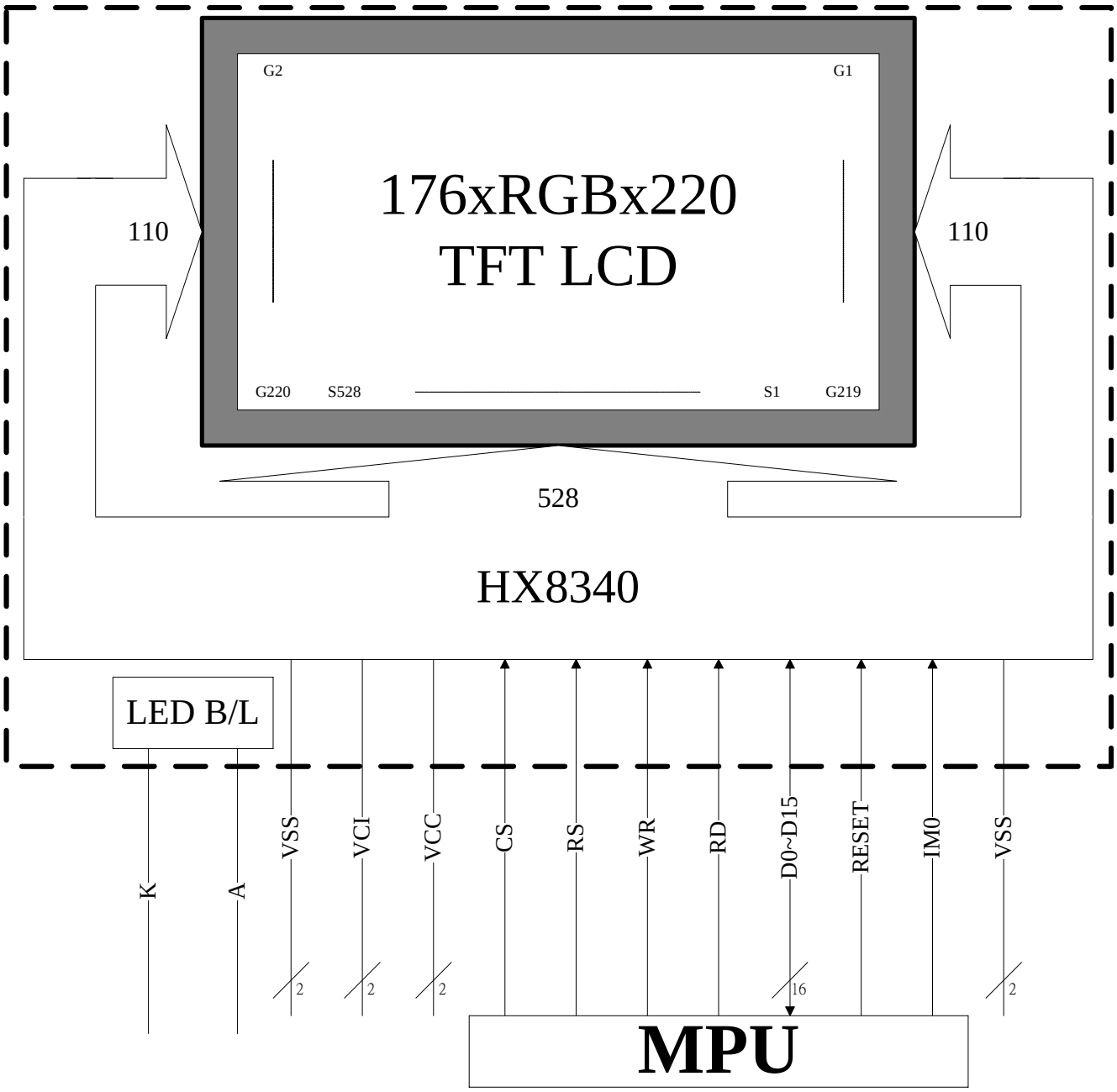
APPROVED		SIGNATURE	
VERSION NO : REV 0			

UNITED RADIANT TECHNOLOGY CORP

APPROVAL	CHECK	DESIGNED BY:
T.Y. HSU	C.K.T.CHEN	Tony CHAN Hunter CHENG Jun/25/07’

Module number	Rev mark	Revision description	Rev by	Rev date
UMNH-7659MD-T	0		T.J Yang Neo Hor	Feb/17/05’
UMNH-7659MD-T	1	Transfer to U.R.T STD.	T.J Yang Neo Hor	Jun/21/05’
UMNH-7659MD-1T	0	Modify polarized from mono type to wide view type.	T.J Yang Neo Hor	Oct/17/05’
UMNH-7659MD-2T	0	Modify polarized from normal type to super wide view type.	T.J Yang Neo Hor	Oct/17/05’
UMNH-7659MD-2T	1	Change the FPC supplier.	Tony CHAN Hunter CHENG	Nov/13/06’
UMNH-7659MD-3T	0	1. Change IC to HX8340. 2. Modify the module number from UMNH-7659FD-2T to UMNH-7659FD-3T.	Tony CHAN Hunter CHENG	Jun/25/07’

BLOCK DIAGRAM:

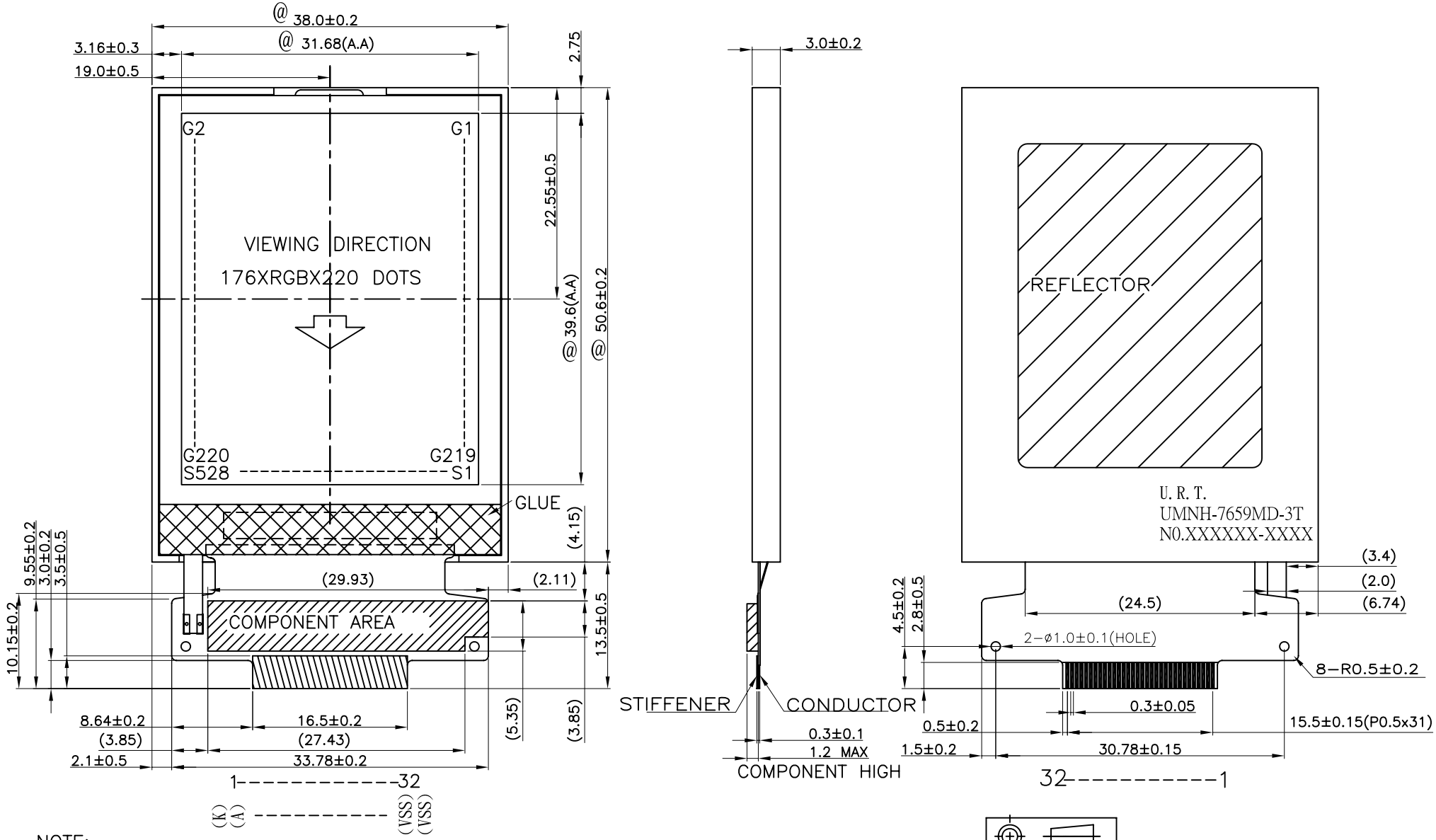


REV.	DATE	MODEL: UMNH-7659MD-3T		
0	Jun/25/07’	TITLE:BLOCK DIAGRAM		
1		APPROVE	CHECK	DESIGN
2				
3		Gary YANG	Tony CHAN	Hunter CHENG
4				
5				

INTERFACE PIN:

Pin No.	Pin Symbol	I/O	Description
1	K	P	Power Supply ground, 0V
2	A	P	Power Supply for LED backlight.
3	VSS	P	Logic ground, 0V
4	VSS	P	Logic ground, 0V
5	VCI	P	Power supply for analog circuit, +2.8V
6	VCI	P	Power supply for analog circuit, +2.8V
7	VCC	P	Power supply for logic, +2.8V
8	VCC	P	Power supply for logic, +2.8V
9	CS	I	Chip select, force Low to active display.
10	RS	I	Register select signal. Low: Index/Status; High: Control register.
11	WR	I	For an 80-system bus interface, serves as a write strobe signal and writes data at the low level.
12	RD	I	For an 80-system bus interface, serves as a read strobe signal and reads data at the low level.
13~28	D0~ D15	I/O	16 bit bi-directional data bus. For an 8 bit interface mode, D8~D15 correspond to D0~D7.
29	RESET	I	Reset pin, active Low.
30	IM0	I	80-system, 8bit or 16-bit interface selects. Low: 16-bit；High: 8-bit.
31	VSS	P	Logic ground, 0V
32	VSS	P	Logic ground, 0V

REV.	DATE	MODEL: UMNH-7659MD-3T		
0	Jun/25/07’	TITLE:INTERFACE PIN		
1		APPROVE	CHECK	DESIGN
2				
3		Gary YANG	Tony CHAN	Hunter CHENG
4				
5				



NOTE:
1. LCD :TFT TRANSMISSIVE TYPE ; NORMAL WHITE
2. Top : −20°C~+70°C, Tst :−30°C~+80°C
3. VIEWING DIRECTION:12 O'CLOCK
4. TFT DRIVER IC : HX8340 OR COMPATIBLE
5. LED BACKLIGHT :WHITE 3 PCS ,VLED=10.8V , IF=20.0mA±5.0mA(CONSTANT VOLTAGE)
6. TOLERANCE FOR NOT ASSIGNED : ±0.3 mm
7. "@": KEY DIMENSION
8. FPC CAN NOT BENDING
9. TFT PIXEL SIZE : 60x180 um

REV	DATE	
0	Jun.25,07'	MODEL: UMNH−7659MD−3T
1		TITLE: OUTLINE DRAWING
2		DESIGN: Tony Chan
3		CHECK: Eric Chen Hunter Cheng
		APPROVAL: Ken LIN

URT−BF103−01A